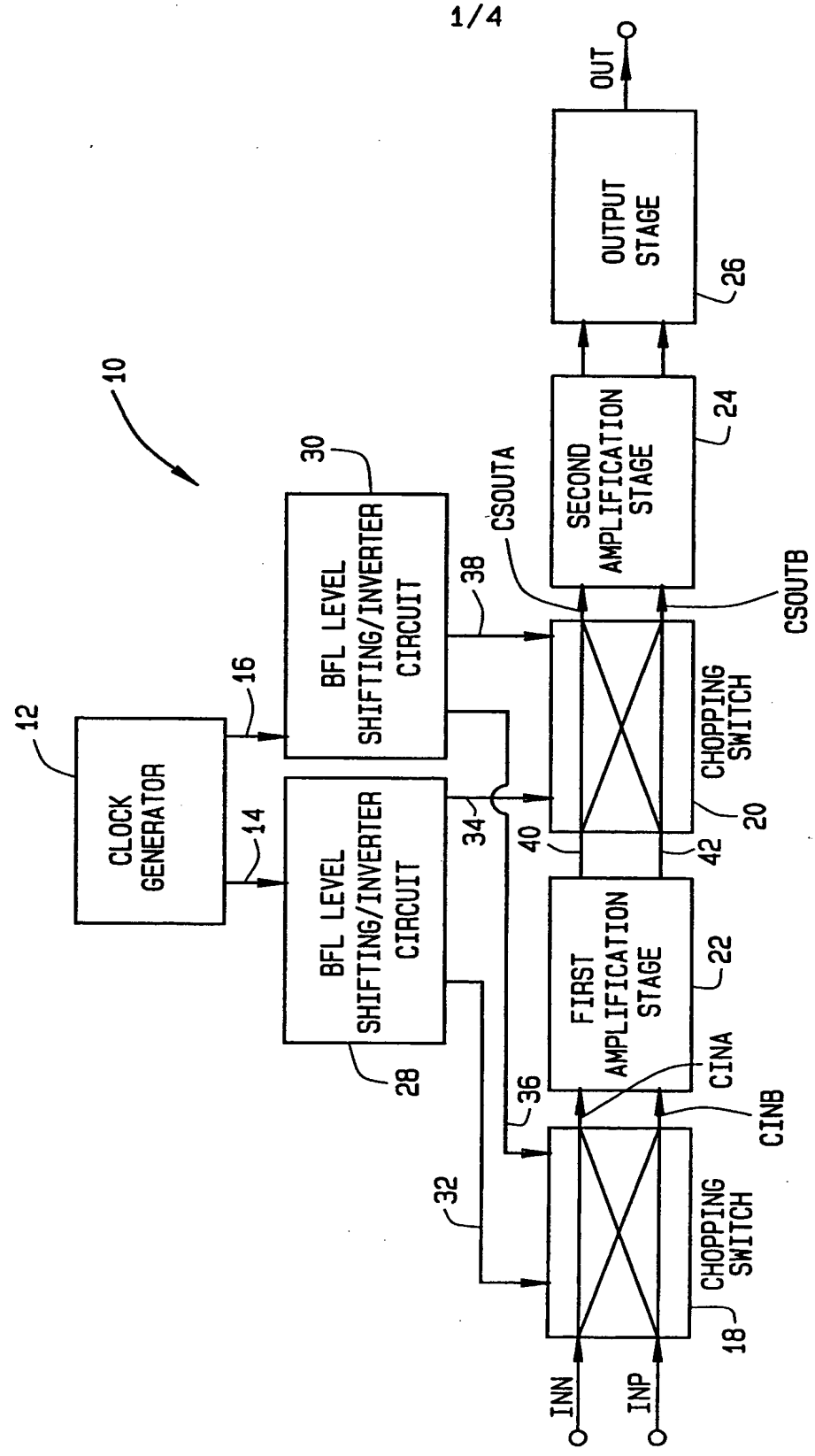
10
12
CLOCK GENERATOR
14
16
BFL LEVEL SHIFTING/INVERTER CIRCUIT
30
BFL LEVEL SHIFTING/INVERTER CIRCUIT
28
34
36
38
40
42
CSOUTA
CSOUTB
CHOPPING SWITCH
20
CHOPPING SWITCH
18
FIRST AMPLIFICATION STAGE
22
SECOND AMPLIFICATION STAGE
24
OUTPUT STAGE
26
32
CINA
CINB
INN
INP
OUT

FIG. 1

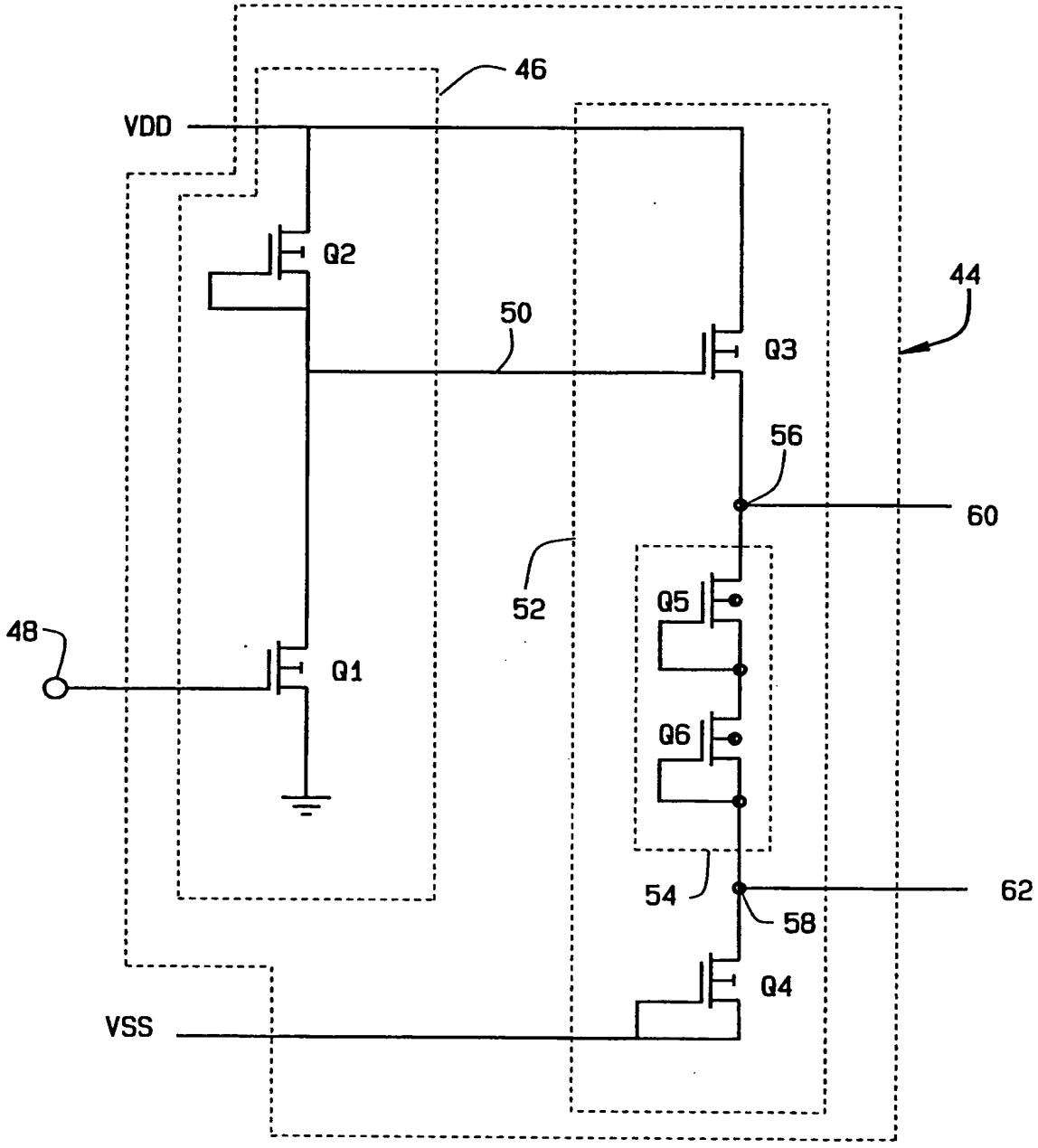

FIG. 2

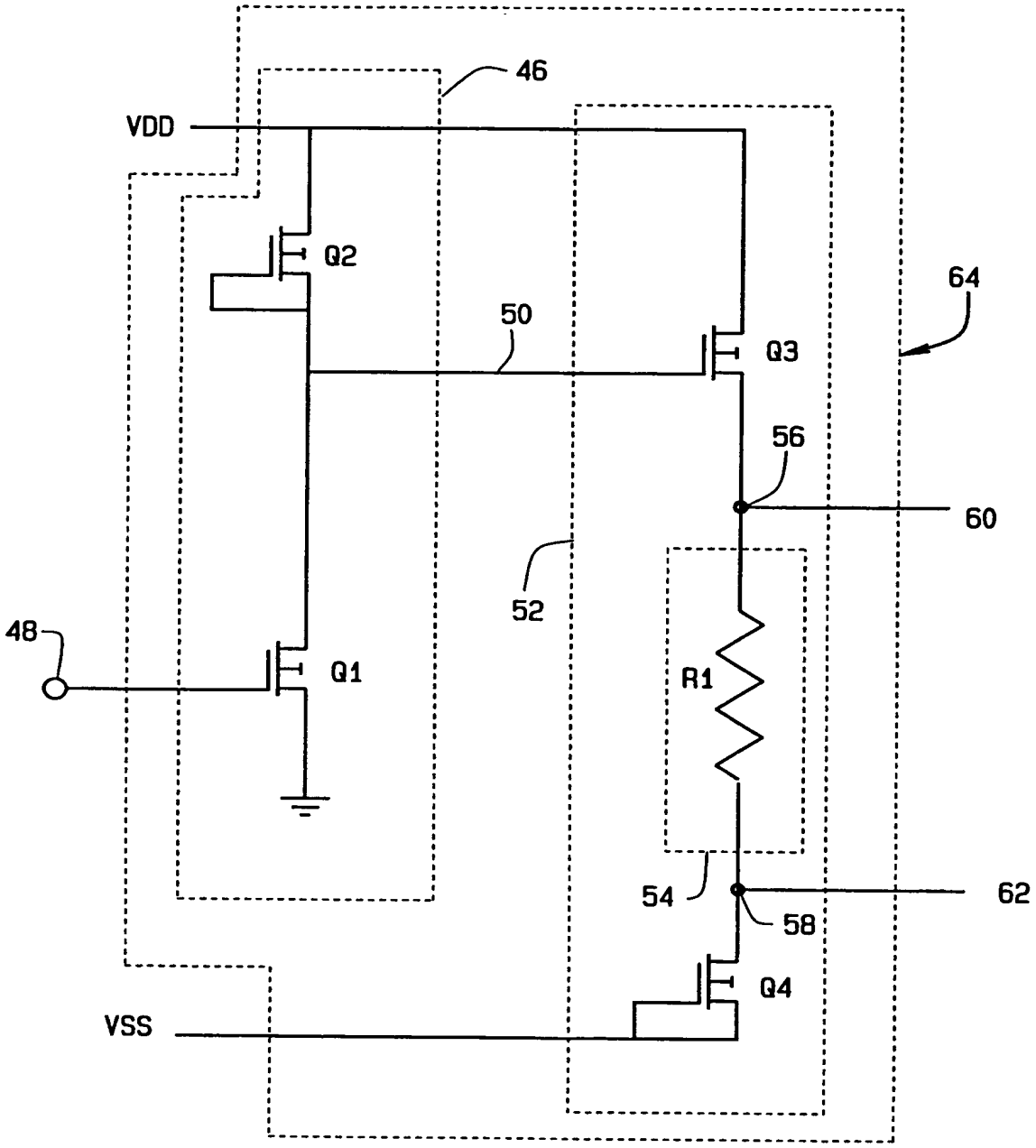

FIG. 3

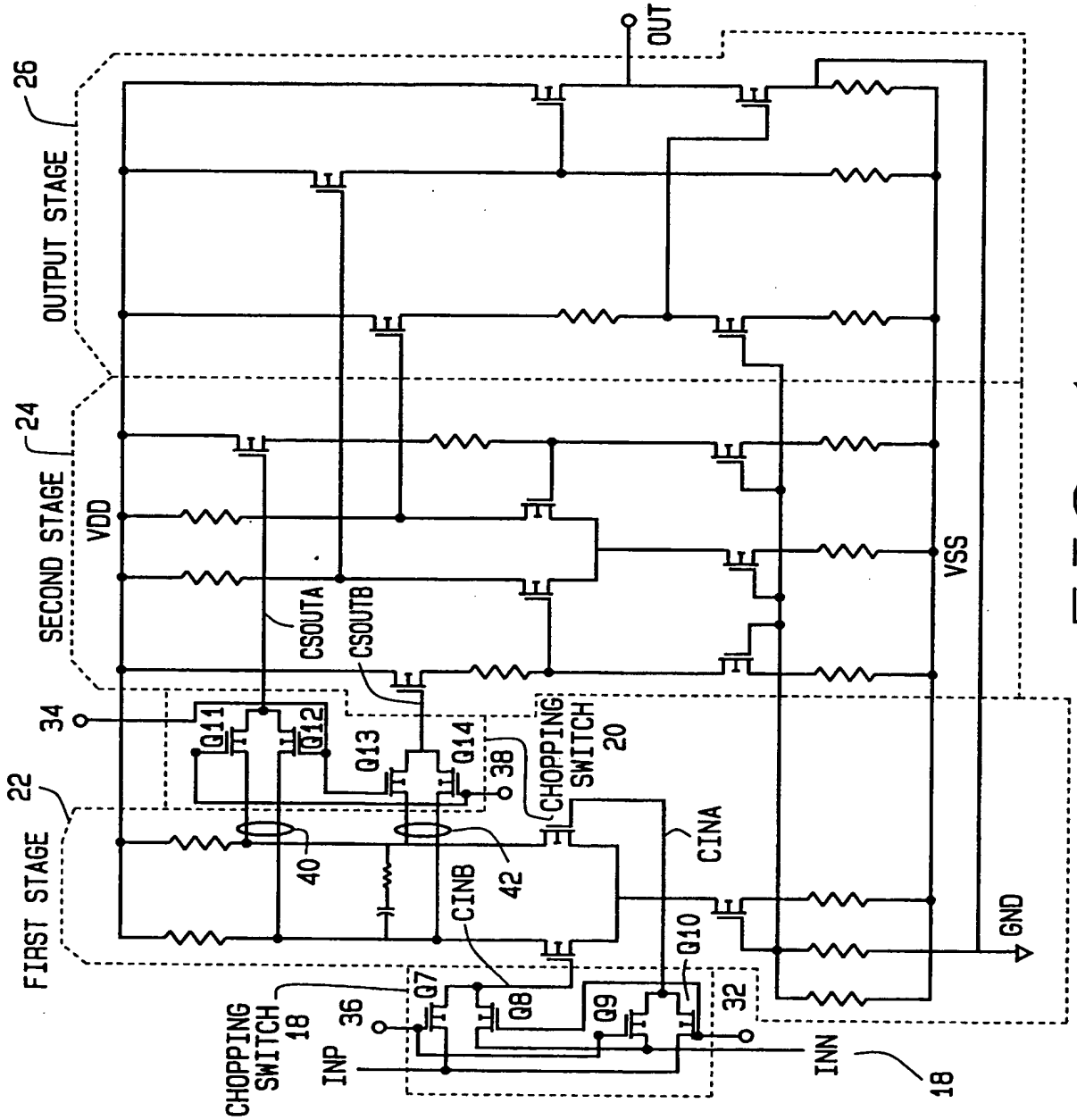
OUT
26
OUTPUT STAGE
24
SECOND STAGE
VDD
CSOUTA
CSOUTB
VSS
34
Q11
Q12
Q13
Q14
38
CHOPPING SWITCH 20
22
FIRST STAGE
40
42
CINB
CINA
GND
Q10
Q7
Q8
Q9
32
CHOPPING SWITCH 18
36
INP
INN
18

FIG. 4